

BONAM VENKATA CHALAMAYYA ENGINEERING COLLEGE

(AUTONOMOUS)

ODALAREVU

ELECTRONIC PULSE

DEPARTMENT OF

ELECTRONICS AND COMMUNICATION ENGINEERING



ELECTRONIC PULSE | VOLUME NO. 19 | ISSUE NO. 1 | 2024-25

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   Department of ECE

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   Roll Number: 23221A0428



ABOUT BONAM VENKATA CHALAMAYYA ENGINEERING COLLEGE

Building Future Engineers Through Excellence & Innovation

Bonam Venkata Chalamayya Engineering College (**Autonomous**) is committed to providing quality technical education through academic excellence, innovation, research, and industry collaboration.

The institution fosters an ecosystem that encourages creativity, ethical values, leadership, and lifelong learning while preparing students to excel in a rapidly evolving technological world.

Autonomous
Academic Excellence

NAAC Accredited
Quality Education

Industry Connect
Strong Collaborations

Excellent Placements
Career Opportunities

ACADEMIC EXCELLENCE
Outcome-based education with modern curriculum and digital learning

INNOVATION & RESEARCH
State-of-the-art labs and research-driven environment to encourage innovation

INDUSTRY COLLABORATION
Strong partnerships with industries for internships, projects and placements

STUDENT DEVELOPMENT
Holistic development through technical clubs, activities and leadership programs

ABOUT ECE DEPARTMENT

The department of Electronics & Communication Engineering bestows the value based technical education and train students to be qualified with an global qualification applicable for building a fortunate career either in Industry/Research or higher education i.e, from A.Y 1997-98 with an intake of 60 students. Presently, the intake of the ECE Department is 180 students. The department is strengthened with 33 well qualified, experienced and dedicated Post graduates with 7 Doctorates and some of them are pursuing Ph.D in different streams.

Zeal and Enthusiasm are the two words which defines the faculty of ECE Department and always been on a high growth path with strong commitment towards engineering education. The Department impulse is to provide a vivid and optimum learning environment to the students in order to help them transcend in today's competition globally. To keep in pace with the current technological trends, the department has established few centre of excellences to incorporate all advancements in collaboration with industries.

The faculty members and students of the department presented research papers in the referred International conference in India and abroad. Research articles were published in International journals. The ECE department has good infrastructure with different labs namely Microwave Engineering lab, Microprocessor Lab, Digital ICs Lab, Communications Lab, EDC Lab, ECA Lab, PDC Lab, LIC Lab, DSP & VLSI Lab. All the labs are fully equipped to cater the present day advanced technology. The department consists of a smart class room facility and department library comprising of different textbooks, magazines and competitive exam preparation books.

Vision of the Department of Electronics and Communication Engineering

To produce globally competent and ethically responsible Electronics and Communication Engineers capable of developing innovative, sustainable, and intelligent communication and electronic technologies to meet industrial and societal needs.

Mission of the Department of Electronics and Communication Engineering

M1

To provide a robust academic environment through learner-centric pedagogy, state-of-the-art laboratory infrastructure, and exposure to emerging technologies like VLSI, IoT, and AI-driven communication systems.

M2

To bridge the gap between academia and industry by fostering collaborative research, innovation, and entrepreneurship through hands-on technical training and real-world problem-solving in electronic and communication technologies.

M3

To instill a strong sense of professional ethics, leadership, and lifelong learning in students, enabling them to develop sustainable and intelligent engineering solutions that address global societal and environmental challenges.

Program Educational Objectives (PEO's)

PEO1:

Graduates will apply strong fundamentals of electronics and communication engineering to analyze, design, and develop innovative electronic and communication systems using modern engineering tools for industrial and societal applications.

PEO2:

Graduates will pursue successful careers in industry, research organizations, and entrepreneurship or engage in higher education, demonstrating innovation, technical competence, and adaptability in emerging areas such as VLSI, IoT, and intelligent communication systems.

PEO3:

Graduates will demonstrate professional ethics, leadership, teamwork, and lifelong learning while developing sustainable engineering solutions to address global technological and societal challenges.

Program Specific Objectives (PSO's)

PSO 1

"Graduates will be able to analyze, design, and implement complex electronic systems and communication networks using modern hardware-software tools in the areas of VLSI, Embedded Systems, IoT, and AI-driven signal processing."

PSO 2

"Graduates will be able to apply ECE principles to develop innovative, cost-effective, and sustainable solutions for real-world industrial and societal problems, while adhering to professional ethics and environmental standards."

Program Outcomes (POs)

PO1: Engineering Knowledge: Apply knowledge of mathematics, natural science, computing, engineering fundamentals and an engineering specialization respectively to develop to the solution of complex engineering problems.

PO2: Problem Analysis: Identify, formulate, review research literature and analyze complex engineering problems reaching substantiated conclusions with consideration for sustainable development.

PO3: Design/Development of Solutions: Design creative solutions for complex engineering problems and design/develop systems/components/processes to meet identified needs with consideration for the public health and safety, whole-life cost, net zero carbon, culture, society and environment as required.

PO4: Conduct Investigations of Complex Problems: Conduct investigations of complex engineering problems using research-based knowledge including design of experiments, modelling, analysis & interpretation of data to provide valid conclusions.

PO5: Engineering Tool Usage: Create, select and apply appropriate techniques, resources and modern engineering & IT tools, including prediction and modelling recognizing their limitations to solve complex engineering problems.

PO6: The Engineer and The World: Analyze and evaluate societal and environmental aspects while solving complex engineering problems for its impact on sustainability with reference to economy, health, safety, legal framework, culture and environment.

PO7: Ethics: Apply ethical principles and commit to professional ethics, human values, diversity and inclusion; adhere to national & international laws.

PO8: Individual and Collaborative Team work: Function effectively as an individual, and as a member or leader in diverse/multi-disciplinary teams.

PO9: Communication: Communicate effectively and inclusively within the engineering community and society at large, such as being able to comprehend and write effective reports and design documentation, make effective presentations considering cultural, language, and learning differences

PO10: Project Management and Finance: Apply knowledge and understanding of engineering management principles and economic decision-making and apply these to one's own work, as a member and leader in a team, and to manage projects and in multidisciplinary environments.

PO11: Life-Long Learning: Recognize the need for, and have the preparation and ability for i) independent and life-long learning ii) adaptability to new and emerging technologies and iii) critical thinking in the broadest context of technological change.

MESSAGE FROM
CHAIRMAN



Dr. Bonam Krishna Satish
Chairman

It gives me immense pleasure to learn that the Department of Electronics and Communication Engineering is bringing out its Technical Magazine. A magazine is a mirror that reflects the ideas, innovativeness, technical competence and the collective efforts of the students and faculty members. It is indeed a matter of pride that our young minds are showcasing their creativity, research insights, and technical skills through this platform.

At our institution, we believe in empowering students with quality education, strong values and a spirit of innovation so that they can excel in a rapidly evolving world. I am confident that this magazine will inspire many more students to explore new ideas, stay curious, and contribute meaningfully to society and the nation.

I extend my heartfelt appreciation to the editorial team, faculty members and students for their dedication and hard work in bringing out this publication. I congratulate everyone involved in this initiative and wish the Department of ECE continued success in all its future endeavors.

May this magazine be a source of knowledge, inspiration and pride for all.

Dream, Innovate, Achieve
Together We Build The Future

MESSAGE FROM SECRETARY



Sri Bonam Kanakayya
Secretary

I am delighted to know that the Department of Electronics and Communication Engineering is bringing out its Technical Magazine. Such initiatives play a vital role in encouraging students to think beyond the classroom, explore new ideas and exhibit their technical expertise.

This magazine is a platform that highlights the academic achievements, innovative projects, research activities and creative talents of our students and faculty members. It reflects the vibrant learning ecosystem and the spirit of excellence that our institution stands for.

Education is not just about gaining knowledge, but also about developing values, leadership qualities and a sense of responsibility towards society. I am confident that through this magazine, our students will be inspired to keep learning, keep innovating and contribute to the progress of the nation.

I appreciate the sincere efforts of the editorial team, faculty and students who have worked dedicatedly to bring out this publication. My best wishes to the Department of ECE for its continuous success and for achieving greater milestones in the years ahead.

May this magazine serve as a source of knowledge, inspiration and pride for everyone associated with it.

Inspire • Innovate • Integrate • Elevate

MESSAGE FROM
PRINCIPAL



Dr. Maheswar Dutta
M.Tech, Ph.D, MSAE, MCI, MISTE

Principal
Bonam Venkata Chalamayya
Engineering College
(Autonomous), Odalarevu

It gives me great pleasure to present the latest edition of the Department of Electronics and Communication Engineering Technical Magazine. This magazine is a reflection of the creativity, innovation and hard work of our students and faculty members. It serves as a platform to showcase their technical competence, research contributions, innovative projects and explorations in emerging technologies.

In today's rapidly evolving world, technical knowledge combined with creativity and ethical values is key to addressing real-world challenges. I am happy to see our students enthusiastically participating in co-curricular and extra-curricular activities, and contributing their ideas towards nation building and societal development.

I appreciate the sincere efforts of the editorial team, faculty coordinators and all the contributors who have worked tirelessly to bring out this magazine. I am confident that this publication will inspire young minds to dream big, work hard and achieve excellence in their professional journeys.

I extend my heartfelt congratulations to the entire team behind this initiative and wish the Department of ECE continued success in all its future endeavors.

Dr. Maheswar Dutta
M.Tech, Ph.D, MSAE, MCI, MISTE

Principal
Bonam Venkata Chalamayya Engineering College
(Autonomous), Odalarevu

MESSAGE FROM HOD



Dr. Akula Pravin
B.Tech., M.Tech., Ph.D.
Head of the Department
Electronics and Communication Engineering

It gives me immense pleasure to present the latest edition of the Department of Electronics and Communication Engineering Technical Magazine. This magazine is a beautiful blend of knowledge, innovation and creativity reflected through the contributions of our talented students and dedicated faculty members.

In today's fast-evolving world, technical competence, research aptitude and continuous learning are essential to meet the challenges of tomorrow. Our department strives to provide a conducive environment that nurtures curiosity, encourages innovation and promotes holistic development of every student.

This magazine serves as an excellent platform for showcasing academic excellence, innovative projects, research activities, technical articles and other achievements. It not only highlights our departmental activities but also inspires others to explore new ideas and push the boundaries of technology.

I sincerely appreciate the efforts of the editorial team, faculty coordinators and student contributors for their hard work and commitment in bringing out this publication. I congratulate everyone associated with this initiative and wish the Department of ECE continued success in all its future endeavors.

Let this magazine be a source of knowledge, inspiration and motivation for all the readers.

INNOVATE Think Beyond | **LEARN** Continuously | **ACHIEVE** Excellence | **INSPIRE** The Future

S. NO	TOPIC
1	AN EFFICIENT DESIGN OF 16-BIT MAC UNIT USING VEDIC MATHEMATICS
2	DESIGN AND IMPLEMENTATION OF ARITHMETIC AND LOGIC UNIT (ALU)
3	SMART HOME ENERGY MANAGEMENT SYSTEM
4	IoT-BASED DISASTER ALERT SYSTEM
5	BRAIN TUMOUR EXTRACTION FROM MRI IMAGES
6	TRAFFIC DENSITY-BASED SIGNAL TIMING CONTROL

AN EFFICIENT DESIGN OF 16-BIT MAC UNIT USING VEDIC MATHEMATICS

ABSTRACT

Multiply and Accumulate (MAC) units form the backbone of many digital signal processing (DSP) applications, influencing critical parameters like power, delay, and area. The need for high-performance and energy-efficient MAC units drives the development of advanced designs, particularly in fields such as image processing, machine learning, and real-time analytics. Traditional DSP systems rely heavily on efficient multipliers to boost speed and reduce power consumption.

This project introduces a 16-bit MAC unit designed with an 8-bit Vedic multiplier based on Urdhva Tiryakbhyam (UT) logic, a principle derived from ancient Indian mathematics. The design also incorporates a carry-save adder (CSA) to efficiently sum partial products, thereby reducing overall computation time. This combination improves upon conventional approaches such as array multipliers and other Vedic multipliers that utilize Square-Root Carry Select Adders (SQRT-CSLA).

The proposed MAC unit was modeled using Verilog HDL and verified through synthesis and simulation using Xilinx ISE Design Suite 14.5 and Vivado 2018.2. Comparative analyses reveal that the design achieves a 9.5% reduction in power consumption while also improving delay and area parameters, making it a promising choice for power-sensitive DSP applications.

Future enhancements could focus on incorporating reversible logic gates to further optimize power and area metrics, paving the way for even more efficient computing architectures. In this project MAC unit is implementing by Vedic mathematics with cadence and tanner tool.



Design and Implementation of Arithmetic and Logic Unit (ALU)



Abstract

The Arithmetic and Logic Unit (ALU) is a core component of modern digital processors such as CPUs, FPUs, and GPUs, performing fundamental arithmetic and logical operations essential to computing systems. This research details the design and implementation of a VLSI-based ALU capable of executing nine key operations: addition, subtraction, multiplication, shifting, comparison, AND, OR, NOT, and XOR. The design leverages advanced techniques like the Carry Look-Ahead Adder (CLA) for high-speed addition and a Vedic multiplier employing the Urdhva Triyambhyam sutra for efficient multiplication. The ALU design is validated at both gate and chip levels using the Xilinx ISE Design Suite 14.7.

The study explores different arithmetic and logical methodologies, optimizing for performance, power consumption, and propagation delay. Each operation is implemented and tested with precise hardware description models in Verilog HDL, ensuring the design's functionality and scalability for applications ranging from basic 4-bit operations to 512-bit implementations. The research also incorporates ripple-carry principles for modular scalability and demonstrates the use of 2x2 and 4x4 Vedic multipliers as foundational building blocks for larger multiplier designs.

Additionally, this ALU supports efficient comparison operations using magnitude comparators and shifting capabilities through a versatile shift register. Logical gates such as AND, OR, NOT, and XOR are implemented with minimal delays, enhancing the ALU's overall efficiency. This design demonstrates how modern VLSI techniques can achieve a balance between complexity and performance, addressing the challenges of nanometer-scale hardware implementations.


ARITHMETIC OPERATIONS


LOGIC OPERATIONS


HIGH SPEED PERFORMANCE


VLSI TECHNOLOGY


SCALABILITY & EFFICIENCY

SMART HOME ENERGY MANAGEMENT SYSTEM



Abstract:

This project aims to promote sustainability by minimizing energy wastage, reducing electricity costs, and lowering the environmental footprint. It also empowers users with data-driven decision-making capabilities to align their energy consumption with greener practices and emerging smart grid technologies. The proposed system demonstrates the potential of IoT in creating energy-efficient smart homes for a sustainable future.

Smart Home Energy Management Systems (SHEMS) have emerged as a promising solution to address the increasing demand for energy efficiency and sustainability in residential settings. By leveraging the power of Internet of Things (IoT) technology, SHEMS enable intelligent control and optimization of energy consumption within homes. This paper explores the key components, functionalities, and benefits of SHEMS. We delve into how these systems collect and analyze real-time energy usage data, automate energy-saving measures, and empower users to make informed decisions about their energy consumption. Additionally, we discuss the potential impact of SHEMS on reducing energy costs, minimizing environmental impact, and enhancing overall comfort and convenience.



IoT-Based Disaster Alert System

ABSTRACT :

Early flood warning systems help communities manage floods by giving accurate information. Recent developments using Internet of Things (IoT) offer early warning system to improve disaster management. A network of sensors collects important information such as rainfall intensity, water levels, and weather conditions. Based on this information, a cloud platform makes reliable flood forecasts and alerts. Traditional flood early warning systems need more benefits of the IoT-based solution. It allows for collecting sensor data and analyzes the real time monitoring in various locations, offering a complete perspective of flood-prone regions. These communications are given to the appropriate persons through various methods, such as mobile applications and alert notifications. It allows for continuous monitoring, which speeds up response times and evacuations. The system is flexible enough to allow more sensors and wider regions of coverage. In addition, it improves flood warning accuracy using IoT and real-time data processing. The system's ability to provide accurate flood alerts at the right moment offers great promise for enhancing disaster management. It communities to make informed decisions, reduces the number of deaths, and reduces floods' economic and social expenses.



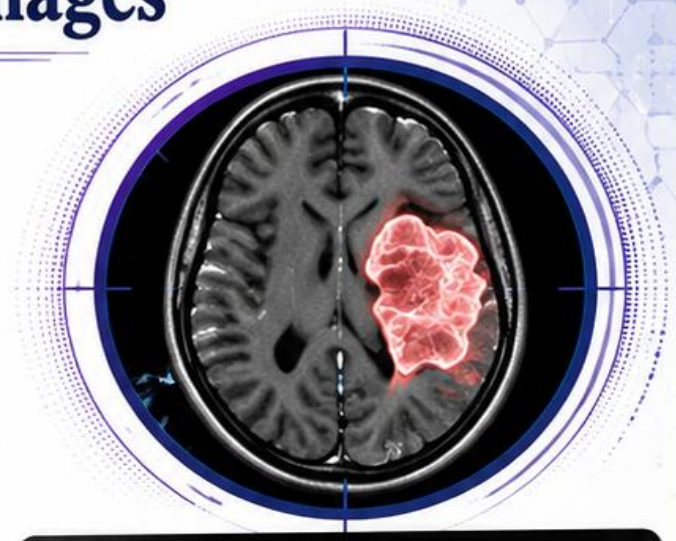
Brain Tumour Extraction from MRI Images

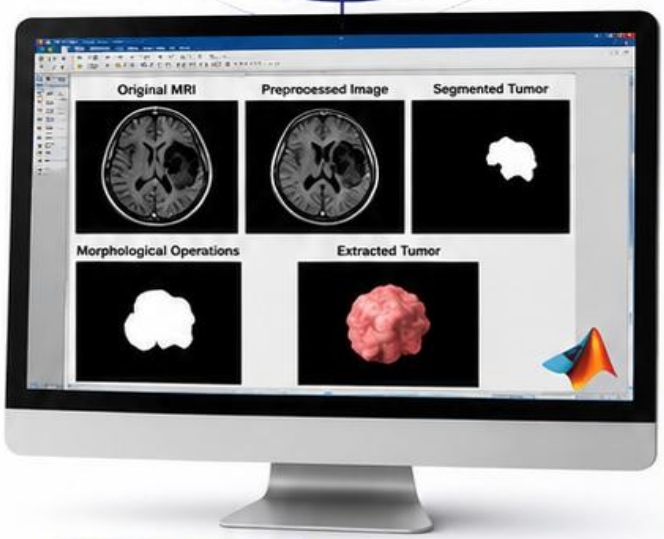
Abstract:

Brain tumors are a severe health concern that require accurate diagnosis and treatment planning for effective patient care. Magnetic Resonance Imaging (MRI) is widely used in medical imaging due to its superior ability to capture detailed brain structures. This project focuses on the development of a MATLAB-based approach for the automated detection and extraction of brain tumors from MRI images. The proposed method employs a combination of advanced image processing techniques to enhance diagnostic precision while reducing manual effort.

The workflow begins with preprocessing steps, including noise removal to eliminate artifacts and improve image quality, ensuring that the critical features of the brain are preserved. Next, segmentation techniques are employed to differentiate the tumor from surrounding tissues, enabling precise isolation of the affected region. Morphological operations are then applied to refine the extracted tumor boundaries, ensuring accurate representation of its size and shape. These techniques are integrated into an efficient algorithm, making the system capable of handling complex and variable MRI images.

The project emphasizes the critical role of automation in medical diagnostics, aiming to reduce human error and expedite the diagnostic process. By leveraging MATLAB's powerful computational and visualization capabilities, this study provides a reliable framework for tumor detection and segmentation. The results demonstrate significant potential in supporting healthcare professionals with accurate and consistent tumor identification, paving the way for enhanced decision-making and treatment planning. This project highlights the synergy between technology and medicine, illustrating how computational tools can improve patient outcomes in the field of medical imaging.





Key Highlights

- Automated detection and extraction of brain tumors using MATLAB
- Advanced image processing: preprocessing, segmentation, and morphological operations
- Accurate tumor isolation for improved diagnostic precision
- Reduces manual effort and supports better clinical decisions

WORKFLOW



TRAFFIC DENSITY-BASED SIGNAL TIMING CONTROL

ABSTRACT

Traffic management in urban areas is a pressing challenge due to increasing vehicle density and inefficient traffic signal timings. Traditional fixed-timing systems often fail to adapt to dynamic traffic conditions, leading to congestion and delays. This project aims to design an intelligent traffic signal control system that adjusts signal timings in real-time based on traffic density at intersections, optimizing the flow of vehicles and minimizing waiting times.

The system uses advanced sensors such as infrared, radar, or cameras to monitor traffic density at each lane of an intersection. Data from these sensors is processed through algorithms that calculate optimal signal durations for each phase. By dynamically adjusting the green signal duration, the system ensures that high-density lanes receive more time, while less congested lanes wait, thereby balancing traffic flow effectively.

The proposed solution integrates Internet of Things (IoT) technologies for real-time communication and control. Traffic data is transmitted to a central server, which processes the information using machine learning or heuristic algorithms. Additionally, the system can provide insights to traffic authorities about peak hours and recurring congestion points, enabling long-term planning and infrastructure improvements.

This smart traffic management system is expected to reduce fuel consumption, air pollution, and commuting times significantly. It enhances the efficiency of urban traffic networks and provides a scalable solution for growing cities. By leveraging technology to address real-world problems, this project contributes to sustainable urban development and improved quality of life for commuters.

INSTITUTION ACTIVITIES



INDUCTION PROGRAMME for newly joining B.Tech. students



Mr.HARISH, Assosiate co-founder of 'Art of living' addressing students on concentration and interview tips.

Mr. Harish, Associate Co-founder of 'Art of living' addressing the students on concentration and interview tips



CHRISTMAS CELEBRATIONS



INDUCTION PROGRAMME for newly joining B.Tech. students



Sankranti Celebrations 2024

Sankranti 2026 was conducted at our college with great joy and enthusiasm. Students and faculty participated in cultural activities, traditional games, and kite flying events. The celebration highlighted the importance of the harvest festival and promoted unity, happiness, and cultural traditions among all participants.



Sankranti Celebrations in Our College

During Sankranti in Konaseema, Prabhala was celebrated with devotion and joy at our college. Students organized a cultural program showcasing decorated prabhala (palanquins) and traditional performances. Folk songs, dances, and festive activities were conducted, reflecting our rich culture, unity, and festive spirit among all participants.



Sankranti Celebrations with Faculty at Our College

Sankranti celebrations were conducted at our college with great joy and enthusiasm. Students and faculty participated in traditional activities, cultural performances, and festive events. The program reflected the importance of Sankranti, promoting unity, culture, and happiness among everyone, making the celebration memorable.

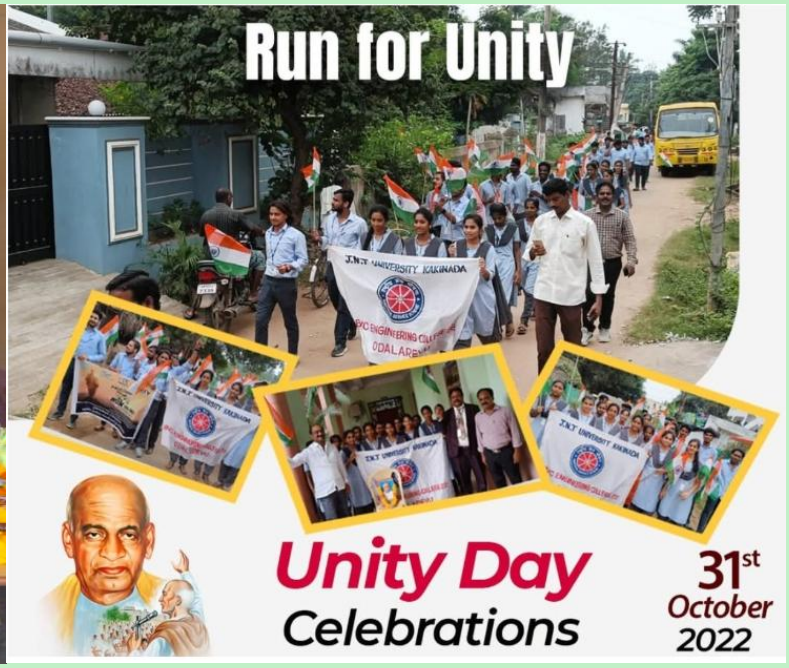


Sankranti Celebrations with Friends

Sankranti 2025 was celebrated with friends with great joy and enthusiasm. We enjoyed traditional games, cultural activities, and delicious festive food together. The day was filled with laughter, fun, and happiness. We also took many pictures to capture beautiful memories. The celebration strengthened our friendship and created a joyful and memorable festive experience for everyone.





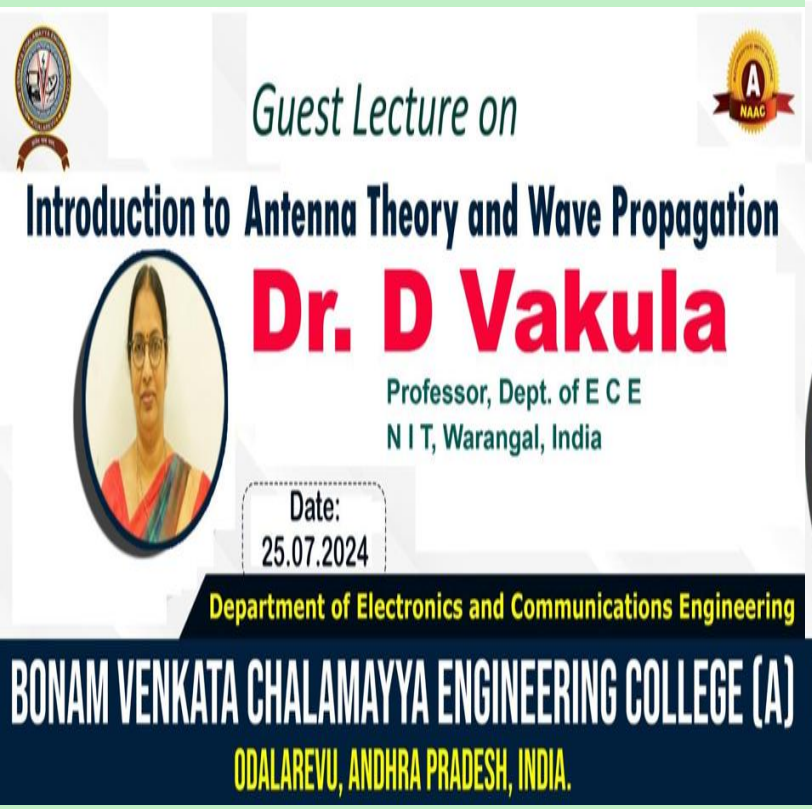


DEPARTMENT ACTIVITIES























Fresher's Day 2024-25

Farewell Day 2024-25

ART GALLERY







Name: G. Durga Devi

Roll No.: 20221A0452





Name: Karanam Uma

Roll No.: 23225A0409







Name: B N Devi

Roll No.: 22225A0403



Name: Karri Uday Sreenu

Roll No.: 22221A0479





Name: M L Jahnavi

Roll No.: 21221A04B0

PLACEMENTS

Regd.No.	Name of the Student	Company Name	Salary/LPA
21221A0401	Achanta Venkata Ganesh	OPPO	1.80 LPA
21221A0402	Achanta Vivek	Scheindner	2.2 LPA
21221A0403	Adabala Naga Ushasri	Skill Intern	4.20 - 5.20 LPA
21221A0407	Adapa Raja Srujana	Scheindner	2.2 LPA
21221A0409	Akana Jyothi Naga Sivaji	Skill Intern	4.20 - 5.20 LPA
21221A0411	Ankam Durga Lakshmana Prasad	Scheindner	2.2 LPA
21221A0412	Aradani Satya Abhish Varma	ZF COMMERTIAL	2.04 LPA
21221A0414	Badri Pardha Sai Bhaskar	Scheindner	2.2 LPA
21221A0415	Balla Sailaja Devi	Scheindner	2.2 LPA
21221A0416	Bandaru Prem Naga Satya Sai	OPPO	1.80 LPA
21221A0418	Battini Vamsi	Skill Intern	4.2-5.2 LPA
21221A0419	Bavireddi Sankararao	Scheindner	2.2 LPA
21221A0420	Beenaboyina Anusha	Scheindner	2.2 LPA
21221A0422	Bokka Durga Srinivas	Qspiders	2-4 LPA
21221A0424	Bonthu Abhishek	Scheindner	2.2 LPA
21221A0426	Bowlada Nagendrababu	ZF COMMERTIAL	2.04 LPA
21221A0428	Budumuri Rajasekhar	Scheindner	2.2 LPA
21221A0431	Chavatapalli Durga Viswas	Scheindner	2.2 LPA
21221A0434	Chinta Veda Vinay Saibabu	Scheindner	2.2 LPA
21221A0435	Chinta Venkateswarlu	ZF COMMERTIAL	2.04 LPA
21221A0437	Chinthapalli Charan	Qspiders	2-4 LPA
21221A0440	Chodapaneedi Vara Prasad	Scheindner	2.2 LPA
21221A0441	Chollangi Chandrika Lakshmi Madhu	Skill Intern	4.20 - 5.20 LPA
21221A0442	Chollangi Sneha Siva Durga Padmini	Scheindner	2.2 LPA
21221A0444	Damisetti Tulasi Sai Praneeth	Scheindner	2.2 LPA
21221A0445	Achanta Naga Durga Prasad	Scheindner	2.2 LPA
21221A0446	Dommeti Ajay	ZF COMMERTIAL	2.04 LPA
21221A0447	Dommeti Naga Venkata Sai Teja	ZF COMMERTIAL	2.04 LPA
21221A0449	Duvva Pavan Kumar	ZF COMMERTIAL	2.04 LPA
21221A0453	Gandham Prabhanjali	Skill Intern	4.20 - 5.20 LPA
21221A0454	Gangumalla Sankar Satya Kumar	Scheindner	2.2 LPA
21221A0456	Golakoti Deepthi	Zitzra	2.24 LPA
21221A0457	Golakoti Pranathi Alekya	Qspiders	2-4 LPA
21221A0458	Golla Surya Chandra Sekhar	Scheindner	2.2 LPA
21221A0460	Gottumukkala Rama Krishna Raju	ZF COMMERTIAL	2.04 LPA
21221A0462	Gubbala Naveen Teja	ZF COMMERTIAL	2.04 LPA
21221A0466	Injavarapu Monika Saraswathi	Scheindner	2.2 LPA
21221A0467	Inumarthi Krishna Vamsi	Scheindner	2.2 LPA
21221A0468	Jagatha Lakshman	Control Print Ltd.	
21221A0470	Jana Pavitra Sai Naga Jyothi	Scheindner	2.2 LPA
21221A0471	Jigireddi Madhu	Scheindner	2.2 LPA
21221A0473	Kadali Triguna Sai	Lucas TVS	2.16-2.28 LPA
21221A0474	Kaila Nagavenkata Teja	Scheindner	2.2 LPA
21221A0475	Kaligithi Chushak	Qspiders	2-4 LPA
21221A0476	Kamisetti Rupesh Durga Subrahmanyam	Qspiders	2-4 LPA
21221A0480	Katakamsetti J. Sai Naga Venkata Lakshmi	Scheindner	2.2 LPA
21221A0483	Kodeti Mahesh	Scheindner	2.2 LPA
21221A0484	Kolisetti Venkat Pavan	Scheindner	2.2 LPA
21221A0486	Komara Uday Kiran	Scheindner	2.2 LPA
21221A0487	Konatham Rashmi Venkatasri	Scheindner	2.2 LPA
21221A0489	Koppisetti Sai Anuja	Scheindner	2.2 LPA
21221A0490	Kudipudi Venkata Siva Sai Manikanta	Scheindner	2.2 LPA
21221A0491	Kudipudi Vineel Durga Bhavadeep	Scheindner	2.2 LPA
21221A0492	Kusumanchi Ch S V Kasi Manikanta	Scheindner	2.2 LPA
21221A0495	Lingala Sudhaveni	Scheindner	2.2 LPA
21221A0497	Lukhman Shaik	Scheindner	2.2 LPA
21221A04A0	Male Naga Ganapathi	Lucas TVS	2.16-2.28 LPA
21221A04A3	Manne Krishna Chaitanya	Lucas TVS	2.16-2.28 LPA
21221A04A6	Medicharla Veera Venkata Naidu	Scheindner	2.2 LPA
21221A04A7	Medisetti Jaya Sri Akhila	Scheindner	2.2 LPA
21221A04A9	Molleti Naga Venkata Durga Purnima	Scheindner	2.2 LPA
21221A04B0	Monuvarthi Lakshmi Jahnavi	Qspiders	2-4 LPA
21221A04B2	Muskudi Renuka	Scheindner	2.2 LPA
21221A04B5	Nalla Sri Nayani	Scheindner	2.2 LPA
21221A04B6	Nallala Siva Ganesh	Scheindner	2.2 LPA
21221A04B9	Nimmakayala Abhi Lashya	Skill Intern	4.20 - 5.20 LPA
21221A04C4	Padala Rohith	Scheindner	2.2 LPA
21221A04C5	Palani Sowjanya	Scheindner	2.2 LPA
21221A04C7	Boppana Surya Teja	Lucas TVS	2.16-2.28 LPA
21221A04C9	Parri Siva Sai Manohara Gowtham Nayudu	Lucas TVS	2.16-2.28 LPA
21221A04D0	Pataballa Sivananaji	Scheindner	2.2 LPA
21221A04D2	Peddireddi Thanuj Sairam	Scheindner	2.2 LPA
21221A04D3	Penumella Venkata Swamymanikanta	Skill Intern	4.20 - 5.20 LPA
21221A04D4	Peruri Sai Devi Sri	Tap Academy	4–7 LPA
21221A04D6	Pilli Satya Veerendra	Scheindner	2.2 LPA
21221A04D7	Polabathula Maha Lakshmi Chandrika	Skill Intern	4.20 - 5.20 LPA
21221A04D8	Polamuri Sandeep Kumar	Qspiders	2-4 LPA
21221A04D9	Poliparthi Lavanya	Scheindner	2.2 LPA
21221A04E2	Pudi Siva Durga Venkata Vinay	Scheindner	2.2 LPA
21221A0436	Chippala Pavani Swarna Satya	TALENT TREK E-LEARNING Pvt Ltd.	4- 6 LPA
21221A0443	Chundru Raj Kumar	Scheindner	2.2 LPA
21221A0450	Dwarapudi Venkata Satya Narayana	Tap Academy	4–7 LPA
21221A0455	Gangumalla Sri Sai Lakshman Vinay	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A0465	Illa Monika	Scheindner	2.2 LPA
21221A0477	Kantamsetti Siddi Vilas	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A0485	Kollaparthi Ajay	Tap Academy	4–7 LPA
21221A0493	Lakkoju Satya	Scheindner	2.2 LPA
21221A04A1	Mandela Jaya Sri Ramanjaneya	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A04B1	Muddana Venkata Mani Subhash	Scheindner	2.2 LPA
21221A04C0	Nimmakayala Raju	Tap Academy	4–7 LPA
21221A04D1	Peddireddi Surya Narayana Murthy	Scheindner	2.2 LPA
21221A04E1	Prathi Mojesh Karunya	Scheindner	2.2 LPA
21221A04F2	Rayapudi Lokesh	Scheindner	2.2 LPA
21221A04F9	Samanasa Lakshmi Narasimha	Scheindner	2.2 LPA
21221A04G6	Seru Shyam Kumar	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A04G9	Sundara Sruthi Sravani	Scheindner	2.2 LPA
22225A0402	Bandaru Asha Nandini	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A04F6	Sabbavarapu Jyothi Venkata Sravani	Tap Academy	4–7 LPA
21221A04F7	Sadhanala Indu Kumar	Scheindner	2.2 LPA
21221A04F8	Saladi Sri Ganesh Narayana	Qspiders	2-4 LPA
21221A04G0	Sanaboina Vijaya Venkata Siva Krishna	Scheindner	2.2 LPA
21221A04G1	Sanga Ravi Teja	Techwave	2.20 LPA
21221A04G3	Satti Surya Narayana Reddy	Scheindner	2.2 LPA
21221A04H0	Talari Ravi	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A04H2	Toram Sai Ramanjaneya Teja	Scheindner	2.2 LPA
21221A04H3	Uddanda Shankar	Scheindner	2.2 LPA
21221A04H4	Undapalli Anulakshmi	TALENT TREK E-LEARNING Pvt Ltd.	4.00- 6.00 LPA
21221A04H5	Undi Gowri Krishna Durga Devi	Scheindner	2.2 LPA
21221A04H6	Vadagana Valli Ananta Sujana	OPPO	1.80 LPA
21221A04H7	Vakkapatla Gopi Veera Ganapathi	Scheindner	2.2 LPA
21221A04H9	Velugubanti Rajya Lakshmi	Scheindner	2.2 LPA
21221A04I0	Vennapu Sai Rama Veera Harini	Scheindner	2.2 LPA
21221A04I1	Yakkala Naga Valli Srujana Devi	Scheindner	2.2 LPA
21221A04I2	Yalla Mahesh	Scheindner	2.2 LPA
21221A04I4	Yalla Sanjay	Lucas TVS	2.16-2.28 LPA
21221A04I6	Yerubandi Bhavana Subrahmanyeswari	Tap Academy	4–7 LPA

ECE TOPPERS

I YEAR / I SEM

S.No	Register No.	Name	SGPA	Rank
1	24221A0463	Isukapatla Gowtham	9.85	1
2	24221A0499	Mandela Sri Latha	9.69	2
3	24221A04B5	Nagabathula Yamini	9.46	3

II YEAR / I SEM

S.No	Register No.	Name	SGPA	Rank
1	23221A04C3	Nimmakayala Sai Lakshmi Mownika	8.95	1
2	23221A04I8	Yedida Venkata Sai Tharun	8.95	1
3	23221A04B9	Nalla Bhavya Naga Anjali	8.80	2
4	23221A04A1	Manchiganti Harika Manga Tayaru	8.80	2
5	23221A0424	Bolireddy Rajkumar	8.80	2
6	23221A0447	Dasari Sri Venkata Harisha	8.80	2
7	23221A0457	Akula Rajeswari	8.80	2
8	23221A0407	Gollapalli Vidya	8.65	3
9	24225A0405	Mandyala Lokesh	8.65	3

III YEAR / I SEM

S.No	Register No.	Name	SGPA	Rank
1	22221A0437	Chodapaneedi Parvathi Devi	8.74	1
2	22221A0452	Dulipudi Pavani Veera Satya	8.74	1
3	22221A04H8	Yelamarthi Satya Vasavi	8.74	1
4	23225A0407	Kancharla Lakshmi Devi	8.74	1
5	22221A04H7	Yedla Sasi Priyanka	8.60	2
6	23225A0414	Mutyala Dhana Durga	8.60	2
7	23225A0417	Sanaka Maheswari	8.60	2
8	22221A0438	Chodapaneedi Rohith S V Mani Atchuta	8.60	2
9	22221A0411	Akula Uma Krishna Veni	8.60	2
10	22221A0444	Dangeti Mounika	8.47	3
11	22221A0473	Kancherla Durga Devi	8.47	3
12	22221A0481	Katta Gowri Priya	8.47	3
13	22221A0482	Katta Sai Srinivas Raghu Praveen	8.47	3
14	22221A04B0	Motupalli Archana S.N.Lakshmi Manga	8.47	3
15	23225A0418	Vadaparthi Sai Durga Vaishnavi	8.47	3
16	23225A0420	Androthu Veera Venkata Padmini	8.47	3

IV YEAR / I SEM

S.No	Register No.	Name	SGPA	Rank
1	21221A0419	Bavireddi Sankararao	9.22	1
2	21221A04J6	Yerramseeti S V N L Sailaja	9.22	1
3	21221A0409	Akana Jyothi Naga Sivaji	8.96	2
4	21221A0441	Chollangi Chandrika Lakshmi Madhu	8.96	2
5	21221A0456	Golakoti Deepthi	8.96	2
6	21221A0442	Chollangi Sneha Siva Durga Padmini	8.83	3
7	21221A0403	Adabala Naga Ushasri	8.83	3
8	22225A0408	Dodda Bala satya Akanksha	8.83	3

I YEAR / II SEM

S.No	Register No.	Name	SGPA	Rank
1	24221A0426	Budithi Hima Bindu	9.85	1
2	24221A0499	Mandela Sri Latha	9.78	2
3	24221A0463	Isukapatla Gowtham	9.41	3

II YEAR / II SEM

S.No	Register No.	Name	SGPA	Rank
1	23221A04C3	Nimmakayala Sai Lakshmi Mownika	9.29	1
2	23221A0423	Bokka Madhusri	9.26	2
3	23221A0424	Bollireddy Rajkumar	9.19	3

III YEAR / II SEM

S.No	Register No.	Name	SGPA	Rank
1	22221A0437	Chodapaneedi Parvathi Devi	9.30	1
2	22221A0449	Dommeti Mouni Varshitha	9.09	2
3	22221A0444	Dangeti Mounika	9.02	3
4	22221A0456	Ganta Kusuma	9.02	3
5	22221A0411	Akula Uma Krishna Veni	9.02	3
6	22221A0427	Bonthu Anusri	9.02	3
7	23225A0414	Mutyala Dhana Durga	9.02	3

IV YEAR / II SEM

s	Register No.	Name	SGPA	Rank
1	21221A0456	Golakoti Deepthi	9.56	1
2	21221A0419	Bavireddi Sankararao	9.32	2
3	22225A0408	Dodda Bala satya Akanksha	9.13	3