

## YEGIREDDI SATYA VINOD



<b>Name</b>	YEGIREDDI SATYA VINOD
<b>Designation</b>	ASSISTANT PROFESSOR
<b>Department</b>	ELECTRONICS AND COMMUNICATION ENGINEERING
<b>Date of Joining in the Institution</b>	11/07/2013
<b>Qualifications</b>	M.Tech.
<b>Total Experience in Years</b>	10
<b>Research Papers Published</b>	Journals: 09; Patents: 03
<b>Research Papers Presented in Conferences</b>	03
<b>Research Areas</b>	Communications , VLSI
<b>Project Guided</b>	UG: 16; PG: 01
<b>Professional Society Memberships</b>	--
<b>Awards</b>	3 TIMES APPRECIATION AWARD FOR GOOD PASS PERCENTAGE
<b>FDPs/Workshops Attended</b>	15
<b>Other Responsibilities</b>	Department Internship Coordinator Design & Simulation Lab In charge Counseling In charge
<b>Contact</b>	Mobile: 9703012229 E-Mail: ysvinod.bvce@bvcgroup.in